

Notice of References Cited	Application/Control No. 10/605,233	Applicant(s)/Patent Under Reexamination LIN, HUI-CHU	
	Examiner Stanetta D. Isaac	Art Unit 2812	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,719,065	02-1998	Takemura et al.	438/163
	B	US-5,940,732	08-1999	Zhang, Hongyong	438/640
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Stanley Wolf and Richard N. Tauber, Silicon Processing For The VLSI Era, Second Edition, pgs. 170-180 and pgs. 202-206
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.